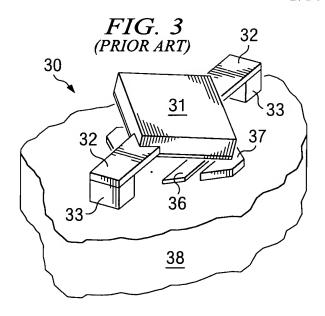


FIG. 2 (PRIOR ART) <u>21</u> 29 23 24 20 کھ 22 26⁾ 23 29 25 25 **~**~28 28a TO CMOS

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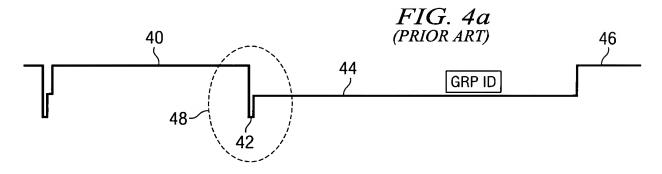
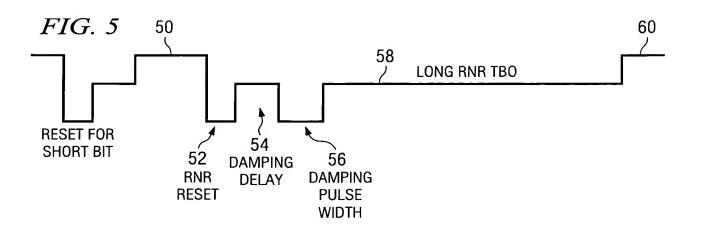
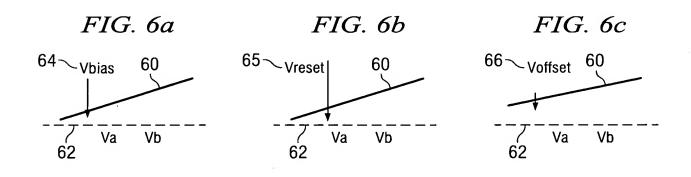
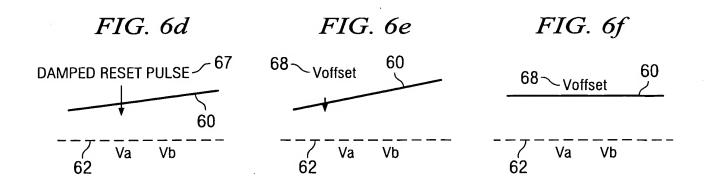


FIG. 4b (PRIOR ART) 30 25 / 40 20 15 44 10 **MIRROR ANGLE** 5 (DEGREES) OR VOLTAGE (V) 0 -5 -10 -15 49 -20 -25 42~ -30 + 50 40 80 30 60 70 90 100 TIME (μ s)

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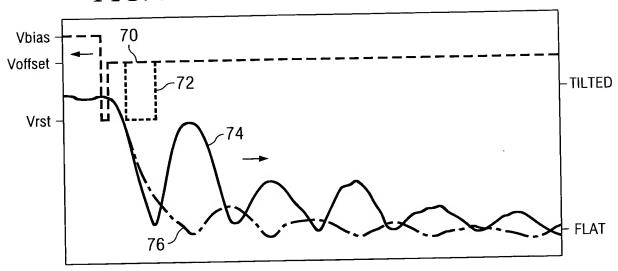


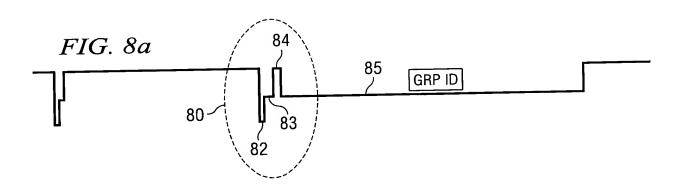


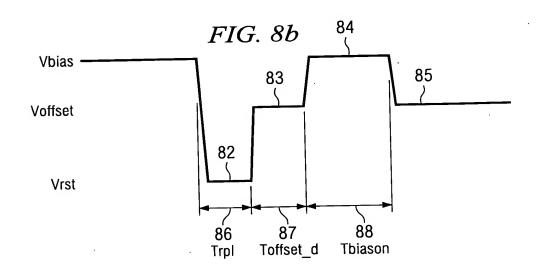


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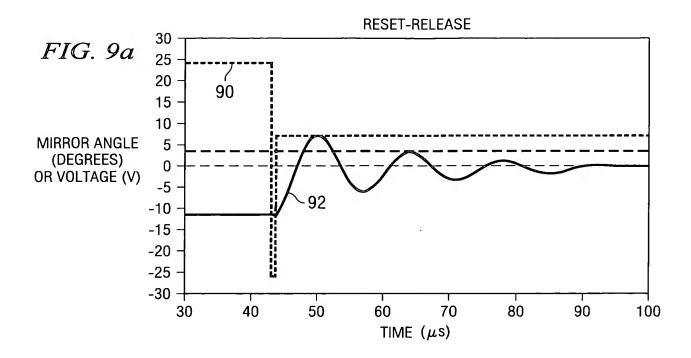
FIG. 7

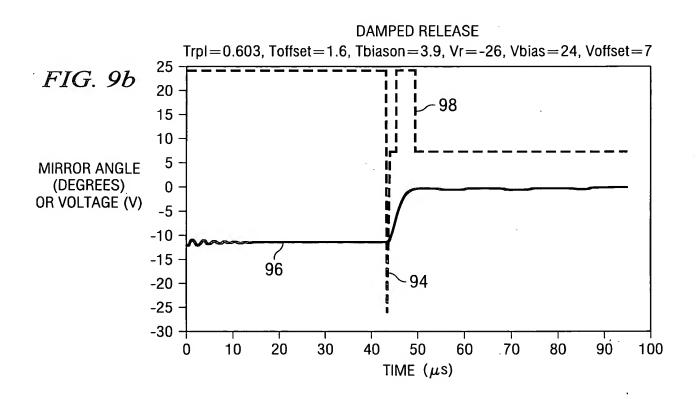






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FIG. 10a

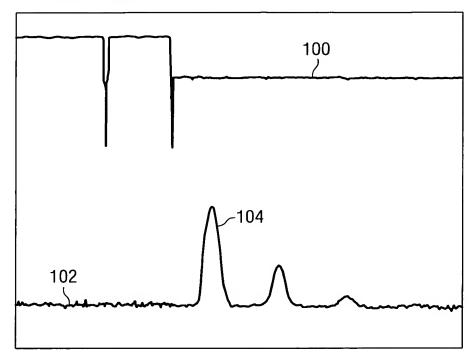
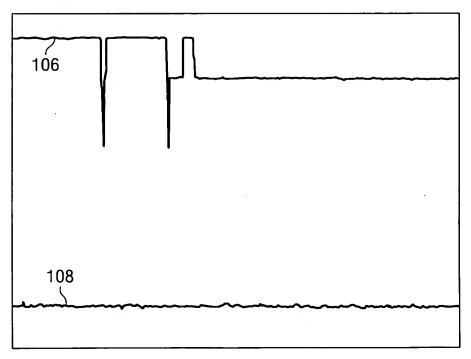


FIG. 10b



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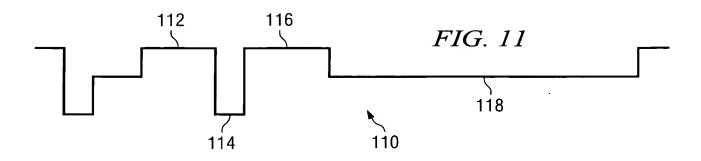


FIG. 12a

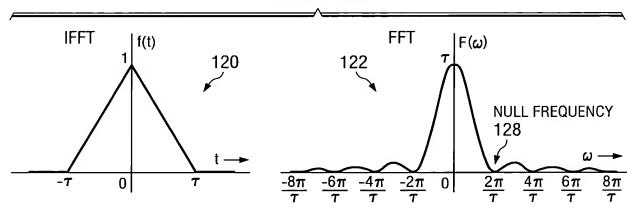
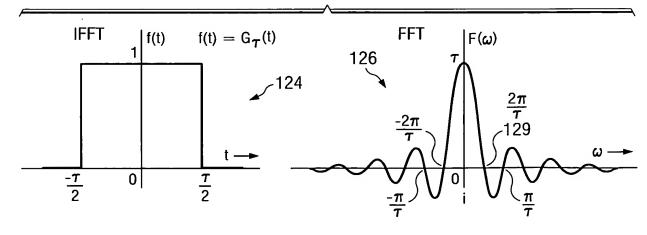
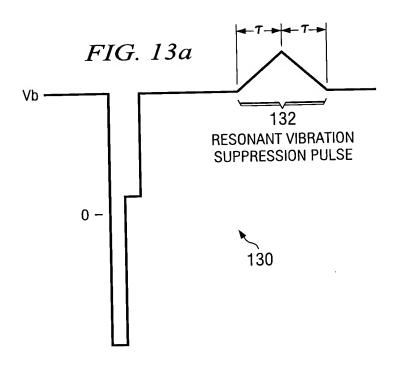
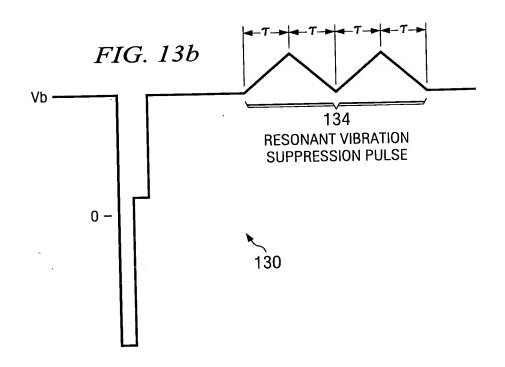


FIG. 12b







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